

DRD710G50

Rectifier Diode

DS5980-1 January 2011 (LN28001)

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V_{RRM}	5000V
I _{F(AV)}	710A
I _{FSM}	11500A

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
DRD710G50 DRD710G48 DRD710G46	5000 4800 4600	$V_{RSM} = V_{RRM} + 100V$

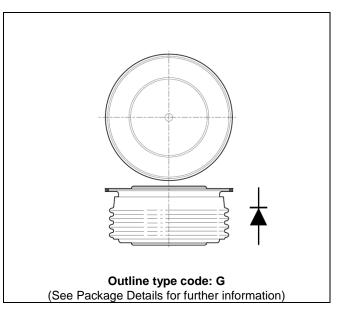


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD710G46 for a 4600V device

CURRENT RATINGS

$T_{\text{case}} = 75^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Double Si	Double Side Cooled					
I _{F(AV)}	Mean forward current	Half wave resistive load	910	А		
I _{F(RMS)}	RMS value	-	1430	А		
I _F	Continuous (direct) on-state current	-	1314	Α		
Single Sid	Single Side Cooled (Anode side)					
I _{F(AV)}	Mean forward current	Half wave resistive load	599	Α		
I _{F(RMS)}	RMS value	-	941	Α		
l _F	Continuous (direct) on-state current	-	814	А		

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Si	Double Side Cooled						
I _{F(AV)}	Mean forward current	Half wave resistive load	710	Α			
I _{F(RMS)}	RMS value	-	1115	Α			
I _F	Continuous (direct) on-state current	-	1000	Α			
Single Sic	le Cooled (Anode side)						
$I_{F(AV)}$	Mean forward current	Half wave resistive load	450	Α			
$I_{F(RMS)}$	RMS value	-	706	Α			
I _F	Continuous (direct) on-state current	-	570	А			

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	9.2	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	0.422	MA ² s
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	11.5	kA
l ² t	I ² t for fusing	$V_R = 0$	0.66	MA ² s

THERMAL AND MECHANICAL RATINGS

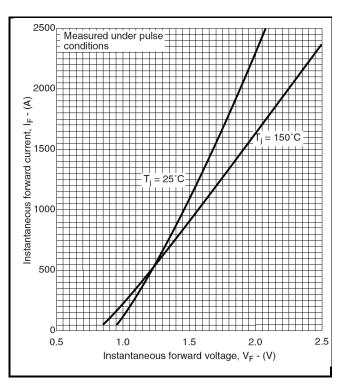
Symbol	Parameter	Test Conditions		Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.032	°C/W
		Single side cooled	Anode DC	-	0.064	°C/W
			Cathode DC	-	0.064	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 12kN	Double side		0.008	°C/W
		(with mounting compound)	Single side	-	0.016	°C/W
T _{vj}	Virtual junction temperature	On-state (conducting)		-	160	°C
		Reverse (blocking)		-	150	°C
T _{stg}	Storage temperature range			-55	175	°C
F _m	Clamping force			11.5	13.5	kN

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CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 1800A peak, T _{case} = 25°C	-	1.8	V
I _{RM}	Peak reverse current	At V _{RRM} , T _{case} = 150°C	-	50	mA
Qs	Total stored charge	$I_F = 1000A$, $dI_{RR}/dt = 3A/\mu s$	-	2600	μC
Irr	Peak reverse recovery current	$T_{case} = 150$ °C, $V_R = 100$ V	-	80	Α
V _{TO}	Threshold voltage	At T _{vj} = 150°C	-	0.88	V
r _T	Slope resistance	At T _{vj} = 150°C	-	0.687	mΩ

CURVES



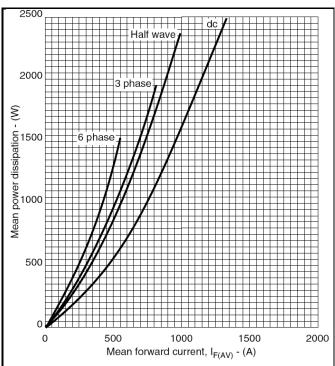


Fig.2 Maximum (limit) on-state characteristics

Fig.3 Dissipation curves

 V_{TM} EQUATION

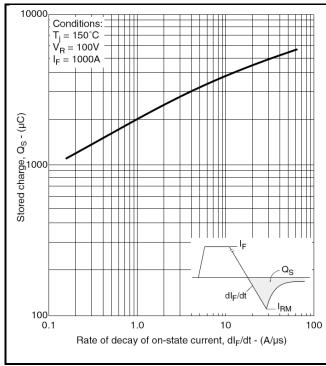
 $V_{TM} = A + Bln (I_T) + C.I_T + D.\sqrt{I_T}$

Where A = 1.183601B = -0.13593

C = 0.000384

D = 0.030400

these values are valid for $T_i = 150$ °C for $I_F 100$ A to 2500A



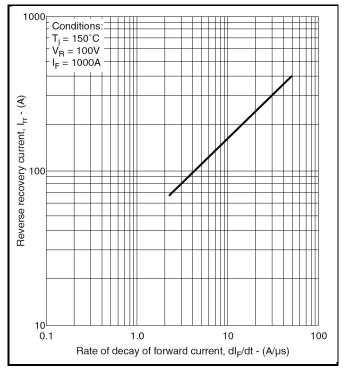
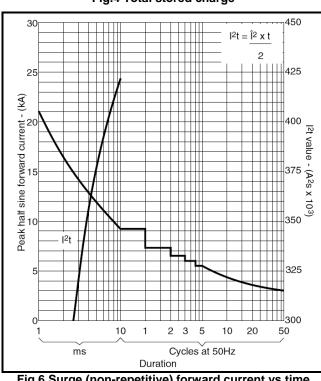
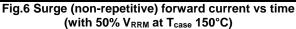


Fig.4 Total stored charge







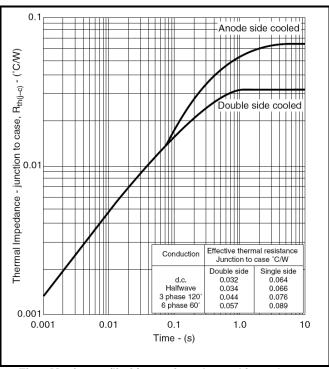
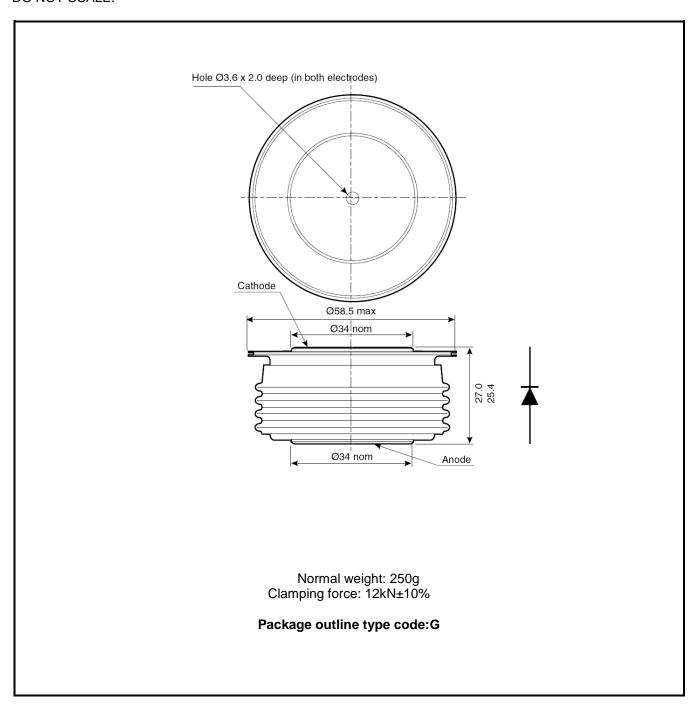


Fig.7 Maximum (limit) transient thermal impedancejunction to case

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.

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